

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of	)	
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Gerwin PREISINGER et al.	)	Group Art Unit: Unassigned
	)	
Application No.: Unassigned	)	Examiner: Unassigned
	)	
Filed: February 5, 2004	)	Confirmation No.: Unassigned
	)	
For: DEVICE FOR PROTECTING A BEARING OF	)	
AN ELECTRICAL MACHINE FROM A	)	
DESTRUCTIVE CURRENT	)	

**FIRST INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, the following information is brought to the Examiner's attention.

Toshihisa SHIMIZU et al., "High Frequency Leakage Current Reduction Based on a Common-Mode Voltage Compensation Circuit," *IEEE*, pages 1961-1967, 1996.

To assist the Examiner in entering the foregoing information into the record, the above-noted document is are cited on the attached form PTO-1449 and a copy of the document is enclosed.

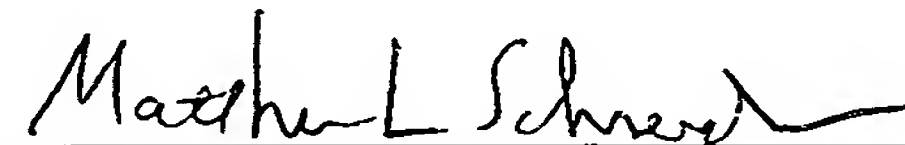
The Examiner is respectfully requested to consider the foregoing information and to acknowledge such consideration by initialling, signing and returning the enclosed additional copy of form PTO-1449.

Respectfully submitted,

BURNS, DOANE, SWECKER, & MATHIS, L.L.P.

Date: February 5, 2004

By:



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Substitute for forms 1449A/PTO & 1449B/PTO  <b>FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	ATTORNEY'S DKT NO. 024943-057	APPLICATION NO. Unassigned
	APPLICANT Gerwin PREISINGER et al.	
	FILING DATE February 5, 2004	GROUP Unassigned

U.S. PATENT DOCUMENTS				
Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Issue/Publication Date (MM-DD-YYYY)

FOREIGN PATENT DOCUMENTS					
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation Yes No

NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	Toshihisa SHIMIZU et al., "High Frequency Leakage Control Reduction Based on a Common-Mode Voltage Compensation Circuit," <i>IEEE</i> , pages 1961-1967, 1996.

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.